

DEVICE AND METHOD FOR DRIVING EL DEVICE

Background of the Invention

5 Technical of the Invention

The present invention relates to a device and method for driving an EL device, and more particularly to a device and method for driving an EL device having a circuit configuration with an output transistor and a diode connected in parallel.

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Related Art

An EL device (Electroluminescence device) has a structure like a condenser of one kind in which an illuminant having a special fluorescent material dispersed and contained in a dielectric material is sandwiched between two sheets of electrodes. When an AC voltage is applied between two sheets of electrodes for this EL device, an AC electric field is applied on the fluorescent material of the illuminant, so that the fluorescent material emits a light and the EL device is lightened.

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20 Generally, it is well known that the luminance and the luminance half life of the EL device depend on the magnitude and frequency of the applied voltage. In order to assure that this EL device has a longer life of the luminance, it is required that the AC voltage without superposition of direct current, i.e., the
25 AC voltage having the same amplitude in positive and negative

regions may be applied between both electrodes of the EL device.

Generally, when controlling a plurality of EL devices, a drive circuit for the EL device is configured, as shown in 5 Fig. 13 or 14. In Fig. 13, the EL devices (L₁, L₂, ..., L_n) are connected in series to the relay switches (S₁, S₂, ..., S_n), respectively, and to an AC power supply 39 for supplying an AC voltage of sinusoidal wave without superposition of direct current to make up a closed circuit. Fig. 14 shows a circuit 10 configuration in which the TRIACs (T₁, T₂, ..., T_n) are employed instead of the relay switches (S₁, S₂, ..., S_n) in Fig. 13. The relay switches and the TRIACs can turn on or off the alternating current.

15 However, since the switches such as relay switches and TRIACs are large in size, the related circuit configuration is unsuitable for the EL devices mostly used to contribute to the miniaturization of the device, making use of the advantages of thin type and high luminance, and remarkably unfavorable 20 particularly when a number of EL devices are driven.

To form an AC power supply circuit as a semiconductor integrated circuit, a circuit configuration as shown in Fig. 15, for example, is considered. In Fig. 15, both electrodes of an EL device L₁ are connected to one electrodes of bipolar 25 transistors (31 to 34), the other electrodes of bipolar

transistors (31 to 34) being connected to a DC power supply and a ground potential point. The transistors (31, 33) connected to the DC power supply and the transistors (32, 34) connected to the ground potential point are turned on and off alternately 5 to apply an AC voltage on the EL device. However, the AC voltages (V_x , V_y) applied to both electrodes of the EL device have a rectangular waveform, as shown in Fig. 16. If the EL device is driven by the AC voltage of this rectangular wave, the load on the EL device is increased owing to a sudden change in the 10 applied voltage, unfavorably resulting in a shorter life of the EL device.

SUMMARY OF THE INVENTION

The present invention has been achieved in the light of 15 the aforementioned problems, and it is an object of the invention to provide an EL device driving device that can drive an EL device with a small and cheap circuit configuration, without shortening the life.

20 It is another object of the invention to provide an EL device driving method that can drive an EL device with a small and cheap circuit configuration, without shortening the life.

25 To accomplish the above object, a first feature of the invention is an EL device driving device comprising,

an EL device having two electrodes;

a first EL driving IC having an output terminal connected to one electrode of the EL device, an input terminal, and means for turning on or off an alternating current flowing between 5 the output terminal and the input terminal;

a second EL driving IC having an output terminal connected to the other electrode of the EL device, an input terminal, and means for turning on or off an alternating current flowing between the output terminal and the input terminal;

10 a first AC power supply for supplying an AC voltage, with one electrode being connected to the input terminal of the first EL driving IC, and the other electrode being connected to a ground potential point; and

15 a second AC power supply for supplying an AC voltage having the same waveform as the AC voltage supplied from the first AC supply, and shifted in phase 180 degrees, with one electrode being connected to the input terminal of the second EL driving IC, and the other electrode being connected to the ground potential point.

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Herein, two electrodes of the EL device are opposed to each other, the output terminals of the first and second EL driving ICs are respectively connected to both electrodes of the EL device, the first AC power supply is connected to the 25 input terminal of the first EL driving IC, and the second AC

power supply is connected to the input terminal of the second EL driving IC. Accordingly, an AC voltage supplied from the first AC power supply is applied via a switching element composed of an output transistor and a diode connected in parallel to 5 one electrode of the EL device, and an AC voltage supplied from the second AC power supply is applied via a switching element composed of an output transistor and a diode connected in parallel to the other electrode of the EL device. Namely, the AC voltage is applied between both electrodes of the EL device, 10 employing the first and second AC power supplies for supplying the AC voltages having the same waveform and shifted in phase 180 degrees.

According to the first feature of the invention, the 15 alternating current can be turned on or off by the switching element composed of the output transistor and the diode connected in parallel to the output transistor. Accordingly, the EL device can be driven employing the AC voltage, without shortening the life, with an integrated (IC) switch for driving the EL device. 20 Moreover, the general-purpose IC containing the switch for this EL driving IC can be utilized. Accordingly, it is possible to provide the EL device driving device that can drive the EL device with a small and cheap circuit configuration, without shortening the life.

As used in the first feature of the invention, the "first EL driving IC" has desirably the output transistor with one electrode connected to the output terminal of the first EL driving IC and the other electrode connected to the input 5 terminal of the first EL driving IC, and the diode connected in parallel to this output transistor, as the "means for turning on or off the alternating current flowing between the output terminal and the input terminal". Similarly, the "second EL driving IC" has desirably the output transistor with one 10 electrode connected to the output terminal of the second EL driving IC and the other electrode connected to the input terminal of the second EL driving IC, and the diode connected in parallel to this output transistor, as the "means for turning on or off the alternating current flowing between the output terminal and the input terminal". Further, the "first EL driving 15 IC" has preferably a shift register circuit for receiving and accumulating the control data from the outside and a latch circuit connected to a control electrode of the "output transistor" for holding the control data accumulated in the 20 shift register circuit. Similarly, the "second EL driving IC" has preferably a shift register circuit for receiving and accumulating the control data from the outside and a latch circuit connected to a control electrode of the "output transistor" for holding the control data accumulated in the 25 shift register circuit.

Herein, the "output transistor" is desirably a bipolar transistor or a field effect transistor. The field effect transistor may be a junction type field effect transistor or 5 a MOS type field effect transistor. The "diode" is connected in parallel between emitter and collector in the case of the bipolar transistor, or between source and drain in the case of the field effect transistor. A forward direction of the diode is defined as one from the emitter to the collector in the case 10 of the npn bipolar transistor, or from the source to the drain in the case of the n-channel MOS type field effect transistor. When the polarity of these transistors is reversed, the forward direction of the diode is also reversed. The "control electrode" means the base electrode in the case of the bipolar transistor, 15 or the gate electrode in the case of the field effect transistor.

The "first AC power supply" and the "second AC power supply" can preferably supply an AC voltage with a frequency from tens to 1000Hz. More preferably, the AC power supply can supply an 20 AC voltage with an amplitude of 50V and a frequency of 400Hz. Further, the "first AC power supply" and the "second AC power supply" may be a household power supply (100Vrms, 60Hz). Further, the AC power supply may generate an AC voltage of predetermined waveform employing an operational amplifier and a low frequency 25 amplifier.

Also, in the first feature of the invention, a plurality of "EL devices" are provided, and the "first EL driving IC" and the "second EL driving IC" may have a plurality of output terminals and a plurality of means for turning on or off the alternating current corresponding to the plurality of EL devices. And the plurality of output terminals for the "first EL driving IC" are connected to one electrodes of the plurality of "EL devices", respectively, and the means for turning on or off the alternating current of the first EL driving IC may turn on or off the alternating current flowing between each of the plurality of output terminals and the input terminal for the "first EL driving IC". Similarly, the plurality of output terminals for the "second EL driving IC" are connected to the other electrodes of the plurality of "EL devices", respectively, and the means for turning on or off the alternating current of the second EL driving IC may turn on or off the alternating current flowing between each of the plurality of output terminals and the input terminal for the "second EL driving IC".

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A second feature of the invention is an EL device driving device comprising,

an EL device having two electrodes;

an EL driving IC having an output terminal connected to

25 one electrode of the EL device, an input terminal, and means

for turning on or off an alternating current flowing between the output terminal and the input terminal;

5 a first AC power supply for supplying an AC voltage, with one electrode being connected to the other electrode of the EL driving IC, and the other electrode being connected to a ground potential point; and

10 a second AC power supply for supplying an AC voltage having the same waveform as the AC voltage supplied from the first AC supply, and shifted in phase 180 degrees, with one electrode being connected to the input terminal of the EL driving IC, and the other electrode being connected to the ground potential point.

15 According to the second feature of the invention, the alternating current can be turned on or off by the switching element composed of the output transistor and the diode connected in parallel to the output transistor. Accordingly, the EL device can be driven employing the AC voltage without superposition of direct current, without shortening the life, and the switch 20 for driving the EL device can be integrated (IC). Moreover, the general-purpose IC containing the switch for this EL driving IC can be utilized. Furthermore, owing to a circuit configuration where the EL driving IC is connected to only one electrode of the EL device, the area for making the circuit can be suppressed 25 to about one-half to simplify the circuit configuration.

Accordingly, it is possible to provide the EL device driving device that can drive the EL device with a small and cheap circuit configuration, without shortening the life.

5 As used in the second feature of the invention, the "EL driving IC" has desirably a plurality of output transistors with one electrode connected to the output terminal of the EL driving IC and the other electrode connected to the input terminal of the EL driving IC, and the diodes connected in parallel to the output transistors. Further, the "EL driving IC" has preferably a shift register circuit for receiving and accumulating the control data from the outside and a latch circuit connected to a control electrode of the "output transistor" for holding the control data accumulated in the shift register circuit.

Herein, the "output transistor" is desirably a bipolar transistor or a field effect transistor.

20 The "first AC power supply" and the "second AC power supply" can preferably supply an AC voltage with a frequency from tens to 1000Hz. More preferably, the AC power supply can supply an AC voltage with an amplitude of 50V and a frequency of 400Hz.

25 Also, in the second feature of the invention, a plurality

of "EL devices" are provided, and the "EL driving IC" may have a plurality of output terminals and a plurality of means for turning on or off the alternating current corresponding to the plurality of EL devices. And the plurality of output terminals 5 for the "EL driving IC" are connected to one electrodes of the plurality of "EL devices", respectively, one electrode of the "first AC power supply" being connected to the other electrodes of the "EL driving elements", and the means for turning on or off the alternating current of the EL driving IC may turn on 10 or off the alternating current flowing between each of the plurality of output terminals and the input terminal for the "EL driving IC".

A third feature of the invention is an EL device driving 15 device comprising,

an EL device having two electrodes;

an EL driving IC having an output terminal connected to one electrode of the EL device, an input terminal connected to a ground potential point, and means for turning on or off 20 an alternating current flowing between the output terminal and the input terminal; and

an AC power supply for supplying an AC voltage without superposition of direct current, with one electrode being connected to the other electrode of the EL driving IC, and the 25 other electrode being connected to the ground potential point.

According to the third feature of the invention, the alternating current can be turned on or off by the switching element composed of the output transistor and the diode connected 5 in parallel to the output transistor. Accordingly, the EL device can be driven employing the AC voltage without superposition of direct current, without shortening the life, and the switch for driving the EL device can be integrated (IC). Moreover, the general-purpose IC containing the switch for this EL driving 10 IC can be utilized. Furthermore, owing to a circuit configuration where the EL driving IC is connected to only one electrode of the EL device, the area for making the circuit can be suppressed to about one-half to simplify the circuit configuration. Furthermore, since the EL driving IC can be operated on the 15 ground potential, there is no need of the electrical insulation employing the photo-coupler or the insulating transformer to receive the data signal, the clock signal and the latch signal from the outside. Also, only one AC power supply may be employed. Accordingly, it is possible to provide the EL device driving 20 device that can drive the EL device with a small and cheap circuit configuration, without shortening the life.

As used in the third feature of the invention, the "EL driving IC" has desirably the output transistor with one 25 electrode connected to the output terminal of the EL driving

IC and the other electrode connected to the input terminal of the EL driving IC, and the diode connected in parallel to the output transistor.

5 Herein, the "output transistor" is desirably a bipolar transistor or a field effect transistor.

The "AC power supply" is preferably able to supply an AC voltage with a frequency from tens to 1000Hz. More preferably, 10 the AC power supply is able to supply an AC voltage with an amplitude of 100V and a frequency of 400Hz.

Also, in the third feature of the invention, a plurality of "EL devices" are provided, and the "EL driving IC" may have 15 a plurality of output terminals and a plurality of means for turning on or off the alternating current corresponding to the plurality of EL devices. And the plurality of output terminals for the "EL driving IC" are connected to one electrodes of the plurality of "EL devices", respectively, one electrode of the 20 "AC power supply" being connected to the other electrodes of the "EL driving elements", and the means for turning on or off the alternating current of the EL driving IC may turn on or off the alternating current flowing between each of the plurality of output terminals and the input terminal for the "EL driving 25 IC".

A fourth feature of the invention is an EL device driving device comprising,

an EL device having two electrodes;

5 an AC power supply for supplying an AC voltage, with one electrode being connected to one electrode of the EL device, and the other electrode being connected to a ground potential point;

10 a first energizing circuit for energizing a first diode connected to the other electrode of the EL device to pass current in a direction from the EL device to the AC power supply;

a second energizing circuit for energizing a second diode connected to the other electrode of the EL device to pass current in a direction from the AC power supply to the EL device; and

15 an energizing control circuit for turning on or off the first and second energizing circuits in synchronism with a positive or negative change in the AC voltage supplied from the AC power supply.

20 According to the fourth feature of the invention, the alternating current flowing through the EL device can be tuned on or off using the first and second energizing circuits having the diode for defining the energizing direction between the EL device and the AC power supply and the energizing control circuit for turning on or off them. Accordingly, the EL device

can be driven employing the AC voltage, without shortening the life, and the switch for driving the EL device can be integrated (IC). Moreover, the general-purpose ICs containing the switch as this EL driving IC can be used. Accordingly, it is possible 5 to provide the EL device driving device that can drive the EL device with a small and cheap circuit configuration and without shortening the life.

10 The "AC power supply" is preferably able to supply an AC voltage with a frequency from tens to 1000Hz. More preferably, the AC power supply is able to supply an AC voltage with an amplitude of 100Vrms and a frequency of 400Hz.

15 Also, in the fourth feature of the invention, a plurality of "EL devices" may be provided, and a plurality of "first energizing circuits" and a plurality of "second energizing circuits" may be provided corresponding to the plurality of EL devices, respectively. And the "energizing control circuit may be configured to turn on or off each of 20 the plurality of first and second energizing circuits corresponding to the plurality of EL devices.

25 Further, in the fourth feature of the invention, one electrode of the "first diode" is connected to the other electrode of the "EL device", and the "first energizing circuit"

has means for enabling the other electrode of the "first diode" to be at the ground potential when the first energizing circuit is turned on. On the other hand, one electrode of the "second diode" is connected to the other electrode of the "EL device", 5 and the other electrode of the "second diode" is connected to the ground potential point.

Also, in the fourth feature of the invention, the "energizing control circuit" turns on the "first energizing 10 circuit", and turns off the "second energizing circuit", while the AV voltage supplied from the "AC power supply" is at a negative potential, and turns off the "first energizing circuit", and turns on the "second energizing circuit", while the AV voltage supplied from the "AC power supply" is at a positive potential.

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A fifth feature of the invention is an EL device driving method to lighten an EL device wherein

when the AC voltage supplied from a first AC power supply is higher than the AC voltage supplied from a second AC power 20 supply, with the same waveform as the AC voltage supplied from the first AC power supply and shifted in phase 180 degrees, a current flows from the first AC power supply through a diode within a first EL driving IC to one electrode of the EL device, and from the other electrode of the EL device through an output 25 transistor in the on state within a second EL driving IC to

the AC power supply; and

when the AC voltage supplied from the first AC power supply is lower than the AC voltage supplied from the second AC power supply, a current flows from the second AC power supply through 5 a diode connected in parallel to the output transistor within the second EL driving IC to the other electrode of the EL device, and from one electrode of the EL device through the output transistor in the on state connected in parallel to the diode within the first EL driving IC to the first AC power supply.

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According to the fifth feature of the invention, the switching element for enabling the alternating current to pass can be constituted by the EL driving IC having the output transistor and the diode connected in parallel to the output 15 transistor. Accordingly, the EL device can be driven employing the AC voltage without superposition of direct current, without shortening the life, and the switch for driving the EL device can be integrated (IC). Moreover, the general-purpose IC containing the switch for this EL driving IC can be utilized. 20 Accordingly, it is possible to provide the EL device driving method that can drive the EL device with a small and cheap circuit configuration, without shortening the life.

A sixth feature of the invention is an EL device driving

25 method to lighten an EL device wherein

when the AC voltage supplied from a first AC power supply is higher than the AC voltage supplied from a second AC power supply, with the same waveform as the AC voltage supplied from the first AC power supply and shifted in phase 180 degrees,

5 a current flows from the first AC power supply to one electrode of the EL device, and from the other electrode of the EL device through an output transistor in the on state within an EL driving IC to the second AC power supply; and

when the AC voltage supplied from the first AC power supply

10 is lower than the AC voltage supplied from the second AC power supply, a current flows from the second AC power supply through a diode connected in parallel to the output transistor within the EL driving IC to the other electrode of the EL device, and from one electrode of the EL device to the first AC power supply.

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According to the sixth feature of the invention, the switching element for enabling the alternating current to pass can be constituted by the EL driving IC having the output transistor and the diode connected in parallel to the output transistor. Accordingly, the EL device can be driven employing the AC voltage without superposition of direct current, without shortening the life, and the switch for driving the EL device can be integrated (IC). Moreover, the general-purpose IC containing the switch for this EL driving IC can be utilized. Furthermore, owing to a circuit configuration where the EL

driving IC is connected to only one electrode of the EL device, the area for making the circuit can be suppressed to about one-half to simplify the circuit configuration. Accordingly, it is possible to provide the EL device driving method that 5 can drive the EL device with a small and cheap circuit configuration, without shortening the life.

A seventh feature of the invention is an EL device driving method to lighten an EL device wherein

10 when the AC voltage without superposition of direct current supplied from an AC power supply is higher than a ground potential, a current flows from the AC power supply to one electrode of the EL device, and from the other electrode of the EL device through an output transistor in the on state within 15 an EL driving IC to the ground potential point; and

when the AC voltage supplied from the AC power supply is lower than the ground potential, a current flows from the ground potential point through a diode connected in parallel to the output transistor in the on state within the EL driving 20 IC to the other electrode of the EL device, and from one electrode of the EL device to the AC power supply.

According to the seventh feature of the invention, the switching element for enabling the alternating current to pass can be 25 constituted by the EL driving IC having the output transistor

and the diode connected in parallel to the output transistor. Accordingly, the EL device can be driven employing the AC voltage without superposition of direct current, without shortening the life, and the switch for driving the EL device can be 5 integrated (IC). Moreover, the general-purpose IC containing the switch for this EL driving IC can be utilized. Furthermore, owing to a circuit configuration where the EL driving IC is connected to only one electrode of the EL device, the area for making the circuit can be suppressed to about one-half to 10 simplify the circuit configuration. Moreover, there is no need of the electrical insulation employing the photo-coupler or the insulating transformer to receive the data signal, the clock signal and the latch signal from the outside. Only one AC power supply may be used. Accordingly, it is possible to provide the 15 EL device driving method that can drive the EL device with a small and cheap circuit configuration, without shortening the life.

Brief Description of the Drawings

20 Fig. 1 is a circuit diagram showing the configuration of an EL device driving device according to a first embodiment of the invention.

Fig. 2 is a circuit diagram showing the configuration of an EL driving IC as shown in Fig. 1 (No.1).

25 Fig. 3 is a circuit diagram showing the configuration

of the EL driving IC as shown in Fig. 1 (No.2).

Fig. 4A is a chart showing a waveform of AC voltage supplied from a first AC power supply as shown in Fig. 1, and Fig. 4B is a chart showing a waveform of AC voltage supplied from a 5 second AC power supply as shown in Fig. 2.

Fig. 5 is a circuit diagram showing the configuration of an EL device driving device according to a second embodiment of the invention.

Fig. 6 is a circuit diagram showing the configuration 10 of an EL device driving device according to a third embodiment of the invention.

Fig. 7 is a chart showing a waveform of AC voltage supplied from an AC power supply as shown in Fig. 6.

Fig. 8 is a circuit diagram showing the configuration 15 of an EL device driving device according to a fourth embodiment of the invention.

Fig. 9 is a circuit diagram showing the configuration of another EL driving circuit as shown in Fig. 8 (No.1).

Fig. 10 is a circuit diagram showing the configuration 20 of another EL driving circuit as shown in Fig. 8 (No.2).

Fig. 11 is a circuit diagram showing the configuration of another EL driving circuit as shown in Fig. 8 (No.3).

Figs. 12A to 12C are charts showing another waveform of AC voltage supplied from the AC power supply as shown in Fig.

25 6.

Fig. 13 is a circuit diagram showing the configuration of the related EL device driving device (No.1).

Fig. 14 is a circuit diagram showing the configuration of the related EL device driving device (No.2).

5 Fig. 15 is a circuit diagram showing the configuration of the related EL device driving device (No.3).

Fig. 16 is a chart showing the waveforms of AC voltage applied on both electrodes of an EL device in Fig. 15.

10 Description of Preferred Embodiment

The preferred embodiments of the present invention will be described below with reference to the drawings. In the drawings, like or the same parts are attached with like or the same numerals.

15 First Embodiment

Fig. 1 is a circuit diagram showing the configuration of an EL device driving device according to a first embodiment of the invention. As shown in Fig. 1, the EL device driving device comprises a plurality of EL devices (L1, L2, ..., Ln) having two electrodes, a first EL driving IC 5 at least having a plurality of output terminals (B1, B2, ..., Bn) respectively connected to electrodes of the plurality of EL devices (L1, L2, ..., Ln) and an input terminal 15, a second EL driving IC 6 at least having a plurality of output terminals (B1, B2, ..., Bn) connected to

the other electrodes of the plurality of EL devices (L1, L2, ..., Ln) and an input terminal 16, a first AC power supply 9 for supplying an AC voltage of sinusoidal wave, with one electrode connected to the input terminal 15 of the first EL driving IC 5 and the other electrode connected to a ground potential point, and a second AC power supply 10 for supplying an AC voltage of sinusoidal wave having the same waveform as the AC voltage supplied by the first AC power supply 9 and shifted in phase 180 degrees, with one electrode connected to the input terminal 16 of the second EL driving IC 6 and the other electrode connected to the ground potential point. In Fig. 1, a first DC power supply 11 supplies an electric power required for the operation of the first EL driving IC 5, and a second DC power supply 12 supplies an electric power required for the operation of the second EL driving IC 6.

Fig. 2 shows a specific circuit configuration of the first and second EL driving ICs (5, 6). The circuits of the first and second EL driving ICs (5, 6) have the same configuration. As shown in Fig. 2, an EL driving IC 5 (or 6) comprises a plurality of output transistors (Q1, Q2, ..., Qn), a plurality of diodes (D1, D2, ..., Dn) respectively connected in parallel to the plurality of output transistors (Q1, Q2, ..., Qn), a latch circuit 4 connected to the control electrodes of the plurality of output transistors (Q1, Q2, ..., Qn), and a shift register circuit 3

connected to the latch circuit 4. One electrodes of the plurality of output transistors (Q1, Q2, ..., Qn) are connected to a plurality of output terminals (B1, B2, ..., Bn), respectively, and the other electrodes are connected to the input terminal 15. The out put 5 transistors (Q1, Q2, ..., Qn), the diodes (D1, D2, ..., Dn), the latch circuit 4 and shift register circuit 3 correspond to a controller in claims.

A data signal for controlling the plurality of EL devices 10 (L1, L2, ..., Ln) to be lightened is sent from the shift resister circuit 3. The control data is accumulated in the shift register circuit 3 in accordance with a clock signal synchronized with the data signal. The control data accumulated in the shift register circuit 3 is received into the latch circuit 4 which 15 receives a latch signal and held. Each output transistor (Q1, Q2, ..., Qn) is turned on or off in accordance with the control data held in the latch circuit 4 connected to a control electrode of each output transistor (Q1, Q2, ..., Qn).

20 The output transistors may be preferably the bipolar transistors (Q1, Q2, ..., Qn) or the field effect transistors. The field effect transistors may be junction type field effect transistors or MOS type field effect transistors (F1, F2, ..., Fn). Diodes (D1, D2, ..., Dn) are connected in parallel between 25 emitter and collector in the case of the bipolar transistors

(Q1, Q2, ..., Qn), or between source and drain in the case of the field effect transistors. A forward direction of the diodes (D1, D2, ..., Dn) is defined as one from the emitter to the collector in the case of the npn bipolar transistors (Q1, Q2, ..., Qn), or from the source to the drain in the case of the n-channel MOS type field effect transistors (F1, F2, ..., Fn). When the polarity of these transistors is reversed, the forward direction of the diodes (D1, D2, ..., Dn) is also reversed. It is required here that the diodes (D1, D2, ..., Dn) of the first EL driving IC 5 and the diodes (D1, D2, ..., Dn) of the second EL driving IC 6 have the same forward direction. The control electrode means the base electrode in the case of the bipolar transistors (Q1, Q2, ..., Qn), or the gate electrode in the case of the field effect transistors (F1, F2, ..., Fn).

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In Fig. 2, the output transistors use the npn bipolar transistors (Q1, Q2, ..., Qn). Fig. 3 shows a circuit configuration of the EL driving IC 5 (or 6) in the case of employing the n-channel MOS type field effect transistors (F1, F2, ..., Fn), instead of the npn bipolar transistors (Q1, Q2, ..., Qn). The output transistors of either type can function as the switching device. In the first embodiment, the bipolar transistors (Q1, Q2, ..., Qn) are employed as the output transistors, and the forward direction of the diodes (D1, D2, ..., Dn) is defined as one from the input terminal 15 (or 16) to the output terminals (B1, B2,

..., Bn).

As shown in Fig. 1, the first EL driving IC 5 operates on an AC voltage supplied from the first AC power supply 9 with respect to the ground potential. Similarly, the second EL driving IC 6 operates on an AC voltage supplied from the second AC power supply 10 with respect to the ground potential. Accordingly, the shift register circuit 3 and the latch circuit 4 can receive the data signal, the clock signal and the latch signal from the outside through a photo-coupler or an insulating transformer due to the necessity of electrical insulation.

Fig. 4A shows a waveform of the AC voltage supplied from the first AC power supply 9. Fig. 4B shows a waveform of the AC voltage supplied from the second AC power supply 10. As shown in Figs. 4A and 4B, the first AC power supply 9 and the second AC power supply 10 have a sinusoidal waveform with the central voltage of $+V_a/2$, and the amplitude of V_a . The first AC power supply 9 and the second AC power supply 10 have the same frequency, but a phase shift of 180 degrees. The first AC power supply 9 and the second AC power supply 10 preferably generate an AC voltage with a frequency from tens to 1000Hz. More preferably, each AC power supply generates an AC voltage with 50Vrms and 400Hz. Moreover, the first and second AC power supplies (9, 10) may be the household power supply (100Vrms, 60Hz).

Furthermore, the first power supply 9 and the second AC power supply 10 may generate an AC voltage of predetermined waveform employing an operational amplifier and a low frequency amplifier.

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A method for driving an EL device with the EL device driving device as shown in Fig. 1 will be described below, regarding the applied voltage and the current path. As shown in Fig. 1, an AC voltage supplied from the first AC power supply 9 is input 10 at the input terminal 15 for the first EL driving IC 5. The AC voltage passes through the output transistors (Q1, Q2, ..., Qn) as shown in Fig. 2 that are connected to the selected EL devices (L1, L2, ..., Ln), namely, the output transistors (Q1, Q2, ..., Qn) being turned on by the data signal sent to the shift 15 register circuit 3. And an AC voltage as shown in Fig. 4A is applied to one electrodes of the EL devices (L1, L2, ..., Ln) selected via the output terminals (B1, B2, ..., Bn) for the first EL driving IC 5, as shown in Fig. 1. On the other hand, an AC voltage supplied from the second AC power supply 10 is input 20 to the input terminal 16 of the second EL driving IC 6. The AC voltage passes through the output transistors (Q1, Q2, ..., Qn) as shown in Fig. 2 which is turned on. An AC voltage as shown in Fig. 4B is applied to the other electrodes of the EL devices (L1, L2, ..., Ln) selected via the output terminals (B1, B2, ..., Bn) for the second EL driving IC 6, as shown in Fig. 25

1. In this way, the AC voltage with the amplitude of 2Va and without superposition of direct current, namely, having the same amplitude in positive and negative regions, is applied between the electrodes of the selected EL devices (L₁, L₂, ..., L_n).

When the AC voltage supplied from the first AC power supply 9 is higher than the AC voltage supplied from the second AC power supply 10, a current flows through the selected EL devices (L₁, L₂, ..., L_n) in the following manner. The current flows from the first AC power supply 9 to the selected EL devices (L₁, L₂, ..., L_n) via the input terminal 15 through the diodes (D₁, D₂ ..., D_n) of the first EL driving IC 5, and via the output terminals (B₁, B₂, ..., B_n) of the first EL driving IC 5. And the current flows from the selected EL devices (L₁, L₂, ..., L_n) to the second AC power supply 10 via the output terminals (B₁, B₂, ..., B_n) of the second EL driving IC 6 through the output transistors (Q₁, Q₂, ..., Q_n) being tuned on for the second EL driving IC 6, via the input terminal 16. On the other hand, when the AC voltage supplied from the first AC power supply 9 is lower than the AC voltage supplied from the second AC power supply 10, a current flows through the selected EL devices (L₁, L₂, ..., L_n) in the following manner. The current flows from the second AC power supply 10 to the selected EL devices (L₁, L₂, ..., L_n) via the input terminal 16 through the diodes (D₁, D₂ ..., D_n)

of the second EL driving IC 6, and via the output terminals (B1, B2, ..., Bn) of the second EL driving IC 6. And the current flows from the selected EL devices (L1, L2, ..., Ln) to the first AC power supply 9 via the output terminals (B1, B2, ..., Bn) of 5 the first EL driving IC 5 through the output transistors (Q1, Q2, ..., Qn) being tuned on for the first EL driving IC 5, via the input terminal 15. In this way, the alternating current flows through the selected EL devices (L1, L2, ..., Ln) to which an AC voltage of sinusoidal wave with the amplitude of 2Va and 10 without superposition of direct current is applied so that the selected EL devices (L1, L2, ..., Ln) are lightened.

As described above, according to the first embodiment of the invention, the alternating current can be tuned on or 15 off using a switching element composed of the output transistors and the diodes connected in parallel to the output transistors. Accordingly, a plurality of EL devices can be driven employing a sinusoidal wave without superposition of direct current, without shortening the life of EL devices, and a number of 20 switches for driving a number of EL devices can be integrated (IC). Moreover, the general-purpose ICs containing a number of switches as this EL driving IC can be used. Accordingly, a number of EL devices can be driven with a small and cheap circuit configuration and without shortening the life.

Second Embodiment

In a second embodiment of the invention, the EL driving IC 5 (or 6) as shown in Fig. 2 or 3 is connected to one electrodes of a plurality of EL devices, and the AC power supply is directly 5 connected to the other electrodes.

Fig. 5 is a circuit diagram showing the configuration of an EL device driving device according to the second embodiment of the invention. As shown in Fig. 5, the EL device driving 10 device according to the second embodiment comprises a plurality of EL devices (L₁, L₂, ..., L_n) having two electrodes, an EL driving IC 6 at least having a plurality of output terminals (B₁, B₂, ..., B_n) connected to one electrodes of the plurality of EL devices (L₁, L₂, ..., L_n) and an input terminal 17, a first AC power supply 15 9 for supplying an AC voltage of sinusoidal wave, with one electrode connected to the other electrodes of the plurality of EL devices (L₁, L₂, ..., L_n) and the other electrode connected to a ground potential point, and a second AC power supply 10 for supplying an AC voltage of sinusoidal wave having the same 20 waveform as the AC voltage supplied by the first AC power supply 9 and shifted in phase 180 degrees, with one electrode connected to the input terminal 17 of the EL driving IC 6 and the other electrode connected to the ground potential point. In Fig. 5, a DC power supply 13 supplies an electric power required for 25 the operation of the EL driving IC 6.

The EL driving IC 6 has the same configuration as described in the first embodiment and shown in Fig. 2 or 3. Namely, the EL driving IC 6 comprises a plurality of output transistors (Q1, Q2, ..., Qn or F1, F2, ..., Fn), a plurality of diodes (D1, D2, ..., Dn) respectively connected in parallel to the plurality of output transistors (Q1, Q2, ..., Qn or F1, F2, ..., Fn), a latch circuit 4 connected to the control electrodes of the plurality of output transistors (Q1, Q2, ..., Qn or F1, F2, ..., Fn), and a shift register circuit 3 connected to the latch circuit 4. One electrodes of the plurality of output transistors (Q1, Q2, ..., Qn or F1, F2, ..., Fn) are connected to a plurality of output terminals (B1, B2, ..., Bn), respectively, and the other electrodes are connected to the input terminal 17.

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The control data accumulated in the shift register circuit 3 is received into the latch circuit 4 which receives a latch signal in accordance with a clock signal synchronized with the data signal, and held. Each output transistor (Q1, Q2, ..., Qn) is turned on or off in accordance with the control data held in the latch circuit 4 connected to a control electrode of each output transistor (Q1, Q2, ..., Qn).

25 The output transistors may be preferably the bipolar transistors (Q1, Q2, ..., Qn) or the field effect transistors

(F₁, F₂, ..., F_n). The field effect transistors may be junction type field effect transistors or MOS type field effect transistors (F₁, F₂, ..., F_n). Diodes (D₁, D₂, ..., D_n) are connected in parallel between emitter and collector in the case of the bipolar transistors (Q₁, Q₂, ..., Q_n), or between source and drain in the case of the field effect transistors. A forward direction of the diodes (D₁, D₂, ..., D_n) is defined as one from the emitter to the collector in the case of the npn bipolar transistors (Q₁, Q₂, ..., Q_n), or from the source to the drain in the case of the n-channel MOS type field effect transistors (F₁, F₂, ..., F_n). When the polarity of these transistors is reversed, the forward direction of the diodes (D₁, D₂, ..., D_n) is also reversed. The control electrode means the base electrode in the case of the bipolar transistors (Q₁, Q₂, ..., Q_n), or the gate electrode in the case of the field effect transistors (F₁, F₂, ..., F_n). In the second embodiment, the output transistors use the npn bipolar transistors (Q₁, Q₂, ..., Q_n), and the forward direction of the diodes (D₁, D₂, ..., D_n) is defined as one from the input terminal 17 to the output terminals (B₁, B₂, ..., B_n).

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The EL driving IC 6 operates on an AC voltage supplied from the second AC power supply 10 with respect to the ground potential. Accordingly, the shift register circuit 3 and the latch circuit 4 can receive the data signal, the clock signal and the latch signal from the outside through a photo-coupler

or an insulating transformer due to the necessity of electrical insulation in the same manner as with the first embodiment.

The AC voltages supplied from the first AC power supply 5 and the second AC power supply 10 have a sinusoidal waveform with the central voltage of $+Va/2$, and the amplitude of Va , as described in the first embodiment and shown in Figs. 4A and 4B. Also, the first AC power supply 9 and the second AC power supply 10 have the same frequency, but a phase shift of 180 degrees. The first AC power supply 9 and the second AC power supply 10 preferably generate an AC voltage with a frequency from tens to 1000Hz. More preferably, each AC power supply generates an AC voltage with 50Vrms and 400Hz. Moreover, the first AC power supply 9 and the second AC power supply 10 may 10 be the household power supply (100Vrms, 60Hz), or may generate 15 an AC voltage of predetermined waveform employing an operational amplifier and a low frequency amplifier.

A method for driving an EL device with the EL device driving 20 device as shown in Fig. 5 will be described below, regarding the applied voltage and the current path. An AC voltage of Fig. 4A supplied from the first AC power supply 9 is directly applied to one electrodes of all the EL devices (L_1, L_2, \dots, L_n). On the other hand, an AC voltage of Fig. 4B supplied from the second 25 AC power supply 10 is input to the input terminal 17. The AC

voltage passes through the output transistors (Q_1, Q_2, \dots, Q_n) being turned on for the EL driving IC 6. And the AC voltage is applied to the other electrodes of the EL devices (L_1, L_2, \dots, L_n) selected via the output terminals (B_1, B_2, \dots, B_n). In 5 this way, the AC voltage with the amplitude of $2V_a$ and without superposition of direct current, namely, having the same amplitude in positive and negative regions, is applied between the electrodes of the selected EL devices (L_1, L_2, \dots, L_n).

10 When the AC voltage supplied from the first AC power supply 9 is higher than the AC voltage supplied from the second AC power supply 10, a current flows through the selected EL devices (L_1, L_2, \dots, L_n) directly from the first AC power supply 9. And the current flows from the selected EL devices (L_1, L_2, \dots, L_n) 15 to the second AC power supply 10 via the output terminals (B_1, B_2, \dots, B_n) through the output transistors (Q_1, Q_2, \dots, Q_n) being tuned on for the EL driving IC 6, via the input terminal 17. Also, an inverse voltage is applied to the output transistors connected to the EL devices (L_1, L_2, \dots, L_n) not selected, namely, 20 the diodes (D_1, D_2, \dots, D_n) connected in parallel to the output transistors (Q_1, Q_2, \dots, Q_n) being turned off to cause an infinite resistance, so that no current flows. On the other hand, if the AC voltage supplied from the first AC power supply 9 is lower than the AC voltage supplied from the second AC power 25 supply 10, a current flows from the second AC power supply 10

via the input terminal 17 through the diodes (D1, D2 ..., Dn) to the selected EL devices (L1, L2, ..., Ln) of the EL driving IC 6, and via the output terminals (B1, B2, ..., Bn). And the current flows from the selected EL devices (L1, L2, ..., Ln) to 5 the first AC power supply 9. Also, a forward voltage is applied to the diodes (D1, D2, ..., Dn) connected in parallel to the output transistors (Q1, Q2, ..., Qn) being turned off, so that a current flows. Namely, a current also flows through the EL devices (L1, L2, ..., Ln) unselected. However, since the EL devices (L1, L2, ..., Ln) are small capacity condensers, and a voltage is only 10 applied in a forward direction of the diodes, the EL devices (L1, L2, ..., Ln) are always charged at about +Va. Namely, it does not occur that the forward current of the diodes only flows through the EL devices (L1, L2, ..., Ln). Accordingly, the EL 15 devices (L1, L2, ..., Ln) unselected are not lightened. Since the life of the EL devices (L1, L2, ..., Ln) is hardly shortened owing to charged state, there is no problem in practice. Accordingly, the selected EL devices (L1, L2, ..., Ln) can be only lightened without having influence on the life of the EL 20 devices (L1, L2, ..., Ln).

As described above, according to the second embodiment of the invention, the alternating current can be tuned on or off using a switching element composed of the output transistors 25 and the diodes connected in parallel to the output transistors

in the same manner as with the first embodiment. Accordingly, a plurality of EL devices can be driven employing a sinusoidal wave without superposition of direct current, without shortening the life of EL devices, and a number of switches 5 for driving a number of EL devices can be integrated (IC). Moreover, the general-purpose ICs containing a number of switches as this EL driving IC can be used. Accordingly, a number of EL devices can be driven with a small and cheap circuit configuration and without shortening the life.

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Further, the circuit configuration, which has the EL driving IC connected to one electrodes of the EL devices, can be simplified while the area for making the circuit is suppressed to about one-half.

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Third Embodiment

In a third embodiment of the invention, the EL driving IC 5 (or 6) as shown in Fig. 2 or 3 is connected to one electrodes of a plurality of EL devices, and the AC power supply is directly 20 connected to the other electrodes. Further, only one AC power supply is employed.

Fig. 6 is a circuit diagram showing the configuration of an EL device driving device according to the third embodiment 25 of the invention. As shown in Fig. 6, the EL device driving

device according to the third embodiment comprises a plurality of EL devices (L₁, L₂, ..., L_n) having two electrodes, an EL driving IC 6 at least having a plurality of output terminals (B₁, B₂, ..., B_n) connected to one electrodes of the plurality of EL devices 5 (L₁, L₂, ..., L_n) and an input terminal 17 connected to the ground potential point, and an AC power supply 14 for supplying an AC voltage of sinusoidal wave without superposition of direct current, with one electrode connected to the other electrodes of the plurality of EL devices (L₁, L₂, ..., L_n) and the other 10 electrode connected to the ground potential point. In Fig. 6, a DC power supply 13 supplies an electric power required for the operation of the EL driving IC 6.

The EL driving IC 6 has the same configuration as described 15 in the first embodiment and shown in Fig. 2 or 3. Namely, the EL driving IC 6 comprises a plurality of output transistors (Q₁, Q₂, ..., Q_n or F₁, F₂, ..., F_n), a plurality of diodes (D₁, D₂, ..., D_n) respectively connected in parallel to the plurality 20 of output transistors (Q₁, Q₂, ..., Q_n or F₁, F₂, ..., F_n), a latch circuit 4 connected to the control electrodes of the plurality of output transistors (Q₁, Q₂, ..., Q_n or F₁, F₂, ..., F_n), and a shift register circuit 3 connected to the latch circuit 4. One electrodes of the plurality of output transistors (Q₁, Q₂, ..., Q_n or F₁, F₂, ..., F_n) are connected to a plurality of output 25 terminals (B₁, B₂, ..., B_n), respectively, and the other electrodes

are connected to the input terminal 17.

The control data accumulated in the shift register circuit 3 is received into the latch circuit 4 which receives a latch 5 signal in accordance with a clock signal synchronized with the data signal, and held. Each output transistor (Q1, Q2, ..., Qn) is turned on or off in accordance with the control data held in the latch circuit 4 connected to a control electrode of each output transistor (Q1, Q2, ..., Qn).

10

The output transistors may be preferably the bipolar transistors (Q1, Q2, ..., Qn) or the field effect transistors (F1, F2, ..., Fn). Diodes (D1, D2, ..., Dn) are connected in parallel between emitter and collector in the case of the bipolar 15 transistors (Q1, Q2, ..., Qn), or between source and drain in the case of the field effect transistors. A forward direction of the diodes (D1, D2, ..., Dn) is defined as one from the emitter to the collector in the case of the npn bipolar transistors (Q1, Q2, ..., Qn), or from the source to the drain in the case 20 of the n-channel MOS type field effect transistors (F1, F2, ..., Fn). When the polarity of these transistors is reversed, the forward direction of the diodes (D1, D2, ..., Dn) is also reversed. The control electrode means the base electrode in the case of the bipolar transistors (Q1, Q2, ..., Qn), or the 25 gate electrode in the case of the field effect transistors (F1,

F₂, ..., F_n). In the third embodiment, the output transistors use the npn bipolar transistors (Q₁, Q₂, ..., Q_n), and the forward direction of the diodes (D₁, D₂, ..., D_n) is defined as one from the input terminal 17 to the output terminals (B₁, B₂, ..., B_n).

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In the third embodiment, unlike the first and second embodiments, the EL driving IC 6 is operated on the ground potential. Accordingly, there is no need of employing the photo-coupler or the insulating transformer for the shift register circuit 3 and the latch circuit 4 to receive the data signal, the clock signal and the latch signal from the outside.

Fig. 7 is a chart showing the waveform of an AC voltage supplied from the AC power supply 14 as shown in Fig. 6. As shown in Fig. 7, the AC voltage supplied from the AC power supply 14 has a sinusoidal waveform without superposition of direct current, namely, having the same amplitude in positive and negative regions. As compared with the waveform of the AC voltage supplied from the first and second AC power supplies (9, 10) as shown in Fig. 4, the frequency is the same but the amplitude is double ($V_b = 2V_a$). The frequency of the AC voltage supplied from the AC power supply 14 is preferably tens to 1000Hz. More preferably, the AC power supply generates an AC voltage with 100Vrms and 400Hz. Moreover, the AC power supply 14 may be the household power supply (100Vrms, 60Hz), or may generate an AC

voltage generated employing an operational amplifier and a low frequency amplifier.

When the AC voltage supplied from the AC power supply 5 14 is plus, a current flows through the EL devices (L₁, L₂, ..., L_n) directly from the AC power supply 14. And the current flows from the EL devices (L₁, L₂, ..., L_n) to the ground potential point via the output terminals (B₁, B₂, ..., B_n) through the output transistors (Q₁, Q₂, ..., Q_n) being tuned on as shown in Fig. 10 2, via the input terminal 17. Also, no current flows through the unselected EL devices (L₁, L₂, ..., L_n), because the output transistors (Q₁, Q₂, ..., Q_n) are turned off, and an inverse voltage is applied to the diodes (D₁, D₂, ..., D_n). On the other hand, if the AC voltage is minus, a current flows from the ground 15 potential point to the AC power supply 14 via the input terminal 17 through the diodes (D₁, D₂, ..., D_n) as shown in Fig. 2, and via the output terminals (B₁, B₂, ..., B_n) to the selected EL devices (L₁, L₂, ..., L_n). And the current flows from the selected EL devices (L₁, L₂, ..., L_n). Also, a forward voltage is applied 20 to the diodes (D₁, D₂, ..., D_n), so that a current flows through the unselected EL devices (L₁, L₂, ..., L_n). However, since the EL devices (L₁, L₂, ..., L_n) are small capacity condensers, and a voltage is only applied in a forward direction of the diodes, the EL devices (L₁, L₂, ..., L_n) are always charged at about +V_a. 25 Namely, it does not occur that the forward current of the diodes

only flows through the EL devices (L₁, L₂, ..., L_n). Accordingly, the EL devices (L₁, L₂, ..., L_n) unselected are not lightened. Since the life of the EL devices (L₁, L₂, ..., L_n) is hardly shortened owing to charged state, there is no problem in practice.

5 Accordingly, the selected EL devices (L₁, L₂, ..., L_n) can be only lightened without having influence on the life of the EL devices (L₁, L₂, ..., L_n).

As described above, according to the third embodiment
10 of the invention, the alternating current can be tuned on or off using a switching element composed of the output transistors and the diodes connected in parallel to the output transistors in the same manner as with the first and second embodiments. Accordingly, a plurality of EL devices can be driven employing
15 a sinusoidal wave without superposition of direct current, without shortening the life of EL devices, and a number of switches for driving a number of EL devices can be integrated (IC). Moreover, the general-purpose ICs containing a number of switches as this EL driving IC can be used. Accordingly,
20 a number of EL devices can be driven with a small and cheap circuit configuration and without shortening the life.

Further, the circuit configuration, which has the EL driving IC connected to one electrodes of the EL devices, can
25 be simplified while the area for making the circuit is suppressed

to about one-half.

Further, the EL driving IC can be operated on the ground potential, there is no need of the electrical insulation through 5 a photo-coupler or an insulating transformer to receive the data signal, the clock signal and the latch signal from the outside. Only one AC power supply may be used. A positive voltage power source and a negative voltage power source are required for supplying the AC power voltage, and the voltage applied 10 on the amplifier for producing the sinusoidal wave may be doubled.

Fourth Embodiment

In a fourth embodiment of the invention, an AC power supply 15 is connected directly to one electrodes of a plurality of EL devices, and a different circuit from the EL driving IC 5 (or 6) as shown in Fig. 2 or 3 is connected to the other electrodes. Further, only one AC power supply is employed.

20 Fig. 8 is a circuit diagram showing the configuration of an EL device driving device according to a fourth embodiment of the invention. As shown in Fig. 8, the EL device driving device according to the fourth embodiment comprises a plurality of EL devices (L₁, L₂, ..., L_n) having two electrodes, an AC power 25 supply 14 for supplying an AC voltage of sinusoidal wave without

superposition of direct current, with one electrode connected to one electrodes of the plurality of EL devices (L₁, L₂, ..., L_n), and the other electrode connected to a ground potential point, and a plurality of EL driving circuits (G₁, G₂, ..., G_n)

5 at least having the output terminals 18 connected to the other electrodes of the plurality of EL devices (L₁, L₂, ..., L_n), and the input terminals 19 connected to the ground potential point.

As shown in Fig. 9, the EL driving circuits (G₁, G₂, ..., G_n) have the same configuration each other. Namely, an EL driving circuit G₁ corresponding to an EL device L₁ comprises a pnp transistor Q₁₁ having an emitter connected to a DC power source 20 for supplying an electric power (= V_{cc}) required for the operation of the EL driving circuit G₁, a voltage drop element 21 such as a Zener diode or resistor which produces a voltage drop of V_{cc} that is a potential of the DC power source 20, with the plus side connected to a collector of the pnp transistor Q₁₁, a first diode D₁₁ having an anode connected to the minus side of the voltage drop element 21, a second diode D₁₂ having 20 an anode connected to a cathode of the first diode D₁₁, and an npn transistor Q₁₂ having a collector connected to the cathode of the second diode D₁₂. An output terminal 18 of the EL driving circuit G₁ is connected at a junction between the cathode of the first diode D₁₁ and the anode of the second diode D₁₂.

The emitter of the npn transistor Q12 is connected to the input terminal 19, the emitter of the pnp transistor Q11 is connected to the base of the pnp transistor Q11 to which a data signal for controlling the lightening of the EL device 5 L1 is input via an inversion circuit 22. The emitter of the npn transistor Q12 is connected to the base of the npn transistor Q12 to which a data signal for controlling the lightening of the EL device L1 is input. Other EL driving circuits (G2, ..., Gn) corresponding to other EL devices (L2, ..., Ln) are configured 10 in the same way as the EL driving circuit G1 corresponding to the EL device L1 as described above.

In this connection, in the fourth embodiment of the invention, a first energizing circuit is made up of the DC power 15 source 20, the pnp transistor Q11, the voltage drop element 21, and the first diode D11 for the EL driving circuit (G1, G2, ..., Gn). A second energizing circuit is made up of the second diode D12 and the npn transistor Q12 for the EL driving circuit (G1, G2, ..., Gn). An energizing control circuit for turning on 20 or off the first and second energizing circuits is made up of the input terminal 19 and the inversion circuit 22.

In the fourth embodiment, the EL driving circuits (G1, G2, ..., Gn) operate on the ground potential in the same manner 25 as the EL driving IC 6 for the third embodiment. Accordingly,

there is no need of using a photo-coupler or an insulating transformer for the pnp transistor Q11 or the npn transistor Q12 to receive the data signal from the outside.

5 Also, in the fourth embodiment, the AC voltage supplied from the AC power supply 14 has the same waveform of Fig. 7 as the AC voltage supplied from the AC power supply 14 in the third embodiment. The frequency of the AC voltage supplied from the AC power supply 14 is preferably tens to 1000Hz. More
10 preferably, the AC power supply generates an AC voltage with 100Vrms and 400Hz. Moreover, the AC power supply 14 may be the household power supply (100Vrms, 60Hz), or may generate an AC voltage employing an operational amplifier and a low frequency amplifier.

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 When the data signal for controlling the lightening of the EL device L1 is turned off, the emitter-collector of the pnp transistor Q11 having no bias applied on the base is placed in the nonconducting state because the potential is equal to
20 Vcc of the DC power source 20 owing to the inversion circuit 22. Also, the emitter-collector of the npn transistor Q12 in which the data signal being turned off directly acts on the base is placed in the nonconducting state, so that the EL driving circuit G1 becomes an open circuit.

On the other hand, when the data signal for controlling the lightening of the EL device L1 is turned on, the emitter-collector of the pnp transistor Q11 having a bias applied on the base is in conduction, because the potential falls to 5 an OFF level of the data signal owing to the inversion circuit 22. Also, the emitter-collector of the npn transistor Q12 in which the data signal being turned on directly acts on the base is placed in conduction, so that the EL driving circuit G1 becomes a closed circuit.

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Then, owing to a voltage drop of Vcc in the voltage drop element 21 having the plus side connected to the DC power source 20 via the pnp transistor Q11, the cathode of the first diode D11 having the anode connected to the minus side of the voltage 15 drop element 21 becomes the ground potential, and the anode of the second diode D12 having the cathode connected via the npn transistor Q12 to the ground potential point becomes the ground potential, so that the output terminal 18 of the EL driving circuit G1 becomes the ground potential.

20

Accordingly, while the AC voltage of sinusoidal wave supplied from the AC power supply 14 is at a minus potential, an alternating current flows through the EL device L1 in a direction from the output terminal 18 at the ground potential 25 that is relatively higher to the AC power supply 14 that is

relatively lower. Conversely, while the AC voltage of sinusoidal wave supplied from the AC power supply 14 is at a plus potential, an alternating current flows through the EL device L1 in a direction from the AC power supply 14 at a positive potential 5 that is relatively higher to the output terminal 18 at the ground potential that is relatively lower, so that the EL device L1 is lightened by the AC voltage.

Therefore, since the ON data signal is input into the 10 EL driving circuits (G1, G2, ..., Gn) corresponding to the selected EL devices (L1, L2, ..., Ln), an alternating current with the energizing direction alternately changing in a direction from the output terminal 18 to the power supply 14 or a direction from the AC power supply 14 to the output terminal 18 flows 15 through the EL devices (L1, L2, ..., Ln), so that the selected EL devices (L1, L2, ..., Ln) are lightened. Also, the unselected EL devices (L1, L2, ..., Ln) have the emitter-collector of the pnp transistor Q11 and the npn transistor Q12 in the nonconducting state, and the EL driving circuit G1 is placed 20 in the open circuit state, so that no current flows through the EL devices (L1, L2, ..., Ln). Accordingly, the unselected EL devices (L1, L2, ..., Ln) are not lightened.

As described above, according to the fourth embodiment 25 of the invention, the alternating current can be tuned on or

off using the first and second energizing circuits having a diode for defining the energizing direction between the EL device and the AC power supply and the energizing control circuit for turning on or off them. Accordingly, a plurality of EL devices 5 can be driven employing a sinusoidal wave without superposition of direct current, without shortening the life of EL devices, and a number of switches for driving a number of EL devices can be integrated (IC). Moreover, the general-purpose ICs containing a number of switches as this EL driving IC can be 10 used. Accordingly, a number of EL devices can be driven with a small and cheap circuit configuration and without shortening the life.

Further, the circuit configuration, which has the EL 15 driving circuit connected to one electrodes of the EL devices, can be simplified while the area for making the circuit is suppressed to about one-half.

Further, the EL driving circuit can be operated on the 20 ground potential, there is no need of the electrical insulation through a photo-coupler or an insulating transformer to receive the data signal from the outside. Only one AC power supply may be used. A positive voltage power source and a negative voltage power source are required for supplying the AC power voltage, 25 and the voltage applied on the amplifier for producing the

sinusoidal wave may be doubled.

The EL driving circuits (G1, G2, ..., Gn) may be configured employing a p-channel field effect transistor F1 and an n-channel 5 field effect transistor F12, as shown in Fig. 10, instead of the pnp transistor Q11 and the npn transistor Q12.

Further, the EL driving circuits (G1, G2, ..., Gn) may be configured, omitting the voltage drop element 21, in such a 10 way that the data signal from the outside acts on the base of the pnp transistor Q11 via a NAND circuit 23, in place of the inversion circuit 22, and the data signal from the outside acts on the base of the npn transistor Q12 via an AND circuit 24, as shown in Fig. 11. The data signal from the outside and the 15 AC voltage of sinusoidal wave in a negative (minus) potential area that is supplied from the AC power supply 14 are input into the NAND circuit 23, and the data signal from the outside and the AC voltage of sinusoidal wave in a positive (plus) potential area that is supplied from the AC power supply 14 20 are input into the AND circuit 24, so that the emitter-collector of the pnp transistor Q11 and the emitter-collector of the npn transistor Q12 can be prevented from conducting to cause a through current flow, without relying on the use of the voltage drop element 21.

In the first to fourth embodiments as described above, the AC voltage applied on the EL devices by the first AC power supply 9 and the second AC power supply 10, or the AC power supply 14, is made to have a sinusoidal waveform, as shown in 5 Fig. 7. However, the waveform of AC voltage to be applied is not limited to the sinusoidal shape, but may be a triangular wave, a trapezoidal wave, or a pseudo-sinusoidal wave, other than the rectangular wave, as shown in Figs. 12A to 12C, for example, so long as the potential is gradually changed without 10 causing significant variation in voltage.

Also, in the first to fourth embodiments as described above, a plurality of EL devices (L1, L2, ..., Ln) are driven corresponding to a plurality of bipolar transistors Q1, Q2, 15 ..., Qn, a plurality of field effect transistors F1, F2, ..., Fn or a plurality of diodes D1, D2, ..., Dn as the output transistors constituting the EL driving ICs 5, 6 and 7, or a plurality of EL driving circuits (G1, G2, ..., Gn). However, the invention is also applicable to such an instance where a single EL device 20 is driven, employing a single output transistor or diode, or a single EL driving circuit constituting the EL driving IC.

As described above, this invention can provide an EL device driving device capable of driving an EL device with a small 25 and cheap circuit configuration, without shortening the life.

Also, this invention can provide an EL device driving method capable of driving an EL device with a small and cheap circuit configuration, without shortening the life.